#### UTC 2SA1300 PNP EPITAXIAL SILICON TRANSISTOR

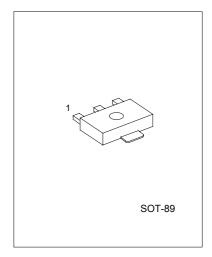
#### SILICON PNP EPITAXAL TYPE

#### **DESCRIPTION**

- \*Strobo Flash Applications.
- \*Medium Power Amplifier Applications.

#### **FEATURES**

- \*High DC Current Gain and Excellent hFE Linearity.
- \*hFE(1)=140-600,  $(V_{CE} = -1V, I_{C} = -0.5A)$
- \*hFE(2)=60(Min.),120(Typ.), $(V_{CE} = -1V,I_{C} = -4A)$
- \*Low Saturation Voltage
- \*V<sub>CE (sat)</sub>= -0.5V(Max.), (I<sub>C</sub>= -2A,I<sub>E</sub>= -50mA)



1: Emitter 2: Collector 3:Base

### ABSOLUTE MAXIMUM RATINGS (TA=25°C)

P.	ARAMETER	SYMBOL	RATIOS	UNIT	
Collector-Base Voltage		VCBO	-20	V	
O-llasta Fasitta Valta a		Vces	-20	V	
Collector-Emitter Voltage		VCEO	-10	V	
Emitter-Base Voltage		VEBO	-6	V	
Collector Current	DC	lc	-2	^	
	Pulsed (Note)	ICP	-5	Α	
Base Current		IB -2		Α	
Collector Power Dissipation		Pc	750	mW	
Junction Temperature		Tj	150	°C	
Storage Temperature Range		Tstg	-55~150	°C	

Note :Pulse Width= 10ms(Max.),Duty Cycle=30%(Max.)

## ELECTRICAL CHARACTERISTICS (TA=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =0	-10	-	-	V
Emitter-collector breakdown voltage	$V_{(BR)EBO}$	$I_E$ = -1mA, $I_C$ =0	-6	-	-	V
Collector cut-off current	I <sub>CBO</sub>	$VCE = -20V, I_E = 0$	-	-	-100	nA
Emitter cut-off current	I <sub>EBO</sub>	$VBE = -6V, I_C = 0$	-	-	-100	nA
DC current Gain	hFE1	VCE= -1V, Ic=0.5A	140	-	600	
	hFE2	VCE= -1V, IC= -4A	60	120	-	
Collector-emitter saturation voltage	Vce(sat)	Ic= -2A, I <sub>B</sub> = -50mA	-	-0.2	-0.5	V
Base-emitter voltage	VBE	VCE= -1V, Ic= -2A	-	-0.83	-1.5	V
Current gain bandwidth product	fT	VCE= -1V,IC= -0.5A	-	140	-	MHz
Output capacitance	Cob	VCE= -10V, IE=0, f=1MHz	-	50	-	pF

CLASSIFICATIONS OF hFE1

	- 1 - 1		
RANK	Y	GR	BL

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QW-R208-012,A

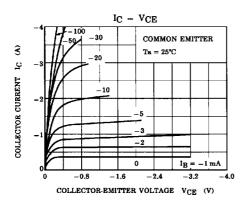
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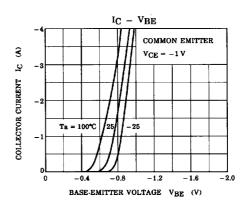
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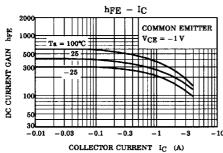
RANGE	140-280	200-400	300-600

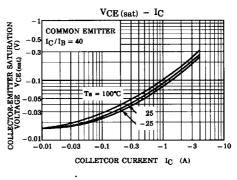
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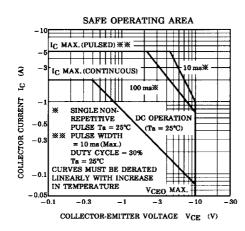
### **CHARACTERITICS CURVE**

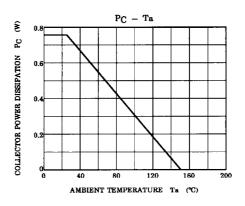












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